



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY, NAGPUR

South Ambazari Road, Nagpur-440010, Maharashtra, India

JRF Advertisement

Date: 18th May, 2022

Applications are invited from motivated and eligible candidates for the post of Junior Research Fellow to work on Science and Engineering Research (SERB) funded research project (No. CRG/2021/005752)

Title of the project	Development of Fully-Automated Liver Cancer Detection and Severity Estimation System from H&E Stained Histopathological Images using Deep Learning-based Image Processing Methods
Position and Number of posts	Junior Research Fellow (One post)
Duration of the Project	03 years
Essential qualification	B.E./B.Tech. and M. Tech/M.E. in Electronics and Communication Engineering and other allied branches of Electronics Engineering / Computer Science or equivalent streams with First Class in both degrees and have Qualified GATE in the past. Candidates, whose M.E./M.Tech results are awaited, can also apply subject to conditions. They will have to submit the result of M.E./M.Tech. at the time of joining.
Desired qualification	1) Good academic record and knowledge in the relevant area (Image Processing, Machine Learning, Programming) 2) Experience in the area of medical image processing. 3) Good analytical ability and technical writing skills.
PhD Opportunity (for GATE qualified)	JRF candidate selected for this position will have the opportunity to register for a regular Ph.D. program in VNIT provided JRF satisfies the eligibility criteria for the same.
Stipend	First two years Rs 31,000/- per month (Consolidated) and Third year Rs. 35,000/- per month (Consolidated).
Nature of post	The post is on contract basis upto a maximum period of three years subject to satisfactory performance evaluation every Six months.
Mode of Application	Interested candidates are requested to submit duly filled application form in the format (available online on the institute website https://www.vnit.ac.in) along with Self Attested Enclosures (CV, Marksheets, Degree certificates, GATE score card, Caste certificate if applicable and any other relevant documents) as a SINGLE PDF FILE through email to the Principal Investigator before the deadlines.
Important Dates	Submission Deadline for Application Form: 20th June, 2022 (Monday) <ul style="list-style-type: none">• Shortlisted candidates will be called for written test and/or interview at a later date which will be intimated by Email.• The selected candidate will be expected to join at the earliest.
Correspondence Address (Principal Investigator)	Dr. Deep Gupta Assistant Professor, Department of Electronics and Communication Engineering, VNIT, South Ambazari Road, Nagpur- 440 010. Phone: +91-712-2801855 (Off) Mobile: +91-9358190782 Email: deepgupta@ece.vnit.ac.in , er.deepgupta@gmail.com
NOTE	1. Selection procedure consists of Written test and/or Interview. 2. The decision of the Director, VNIT Nagpur will be final in all respects.

Dr. Deep Gupta, Principal Investigator