

DEPARTMENT OF ELECTRICAL ENGINEERING

VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY

South Ambazari Road, Nagpur - 440010

Maharashtra

Advertisement

Date: 19/01/2022

Call for the position of Junior Research Fellow for the DST-GITA India-Taiwan S&T Cooperation Research Project "Highly Efficient and Reliable Solid State Transformer for Future

Distribution Network with Renewable Energy Support"

Applications are invited from motivated and eligible candidates for the position of Junior Research Fellow (JRF) for the **Department of Science and Technology (DST-GITA) India-Taiwan S&T Cooperation Programme** sponsored research project on "**Highly Efficient and Reliable Solid State Transformer for Future Distribution Network with Renewable Energy Support**".

Essential Qualifications:

- 1. M. Tech/M. E. in Electrical Engineering Department with specialization in Power Electronics/Electrical Drives/Power System/Control System or equivalent.
- 2. Candidate should have qualified GATE in the past.

Desirable Qualifications:

- 1. Candidate should have experimental knowledge in the **hardware/control design of power electronic converters**. First division in UG and PG will be given preference.
- 2. Proficient in MATLAB, PSIM, PCB Design Software and Microcontroller/Digital Signal Controller.
- 3. Good analytical ability and competent in English writing,

Fellowship:

First two years Rs. 31,000 p.m. (Consolidated) and Third year Rs. 35,000 (consolidated). HRA will be given as per rules.

Duration:

Maximum duration of the project is three years. Initially, appointment will be made for six months, which can be extended later for every six months till the completion of project subject to approval of Director, VNIT. The extension shall be solely based on satisfactory performance.

How to apply:

Interested candidates are requested to submit duly filled and signed application form in proper format (available online on institute website) and detailed Curriculum Vitae through **email** and **hard copy**. Please stick to the deadlines mentioned below. Along with the application, candidates have to send (A) Two Reference Letters, and (B) Maximum One Page details on the skills and achievements.

Note:

- 1. No TA/DA will be paid to appear for written test/interview.
- 2. Candidate selected for this position can also have chance to register for regular PhD program provided they satisfy the eligibility criteria for PhD program.
- 3. Decision of Director VNIT Nagpur will be the final in all respect.

Correspondence address (Principal Investigator):

Dr. Pradyumn Chaturvedi, Assistant Professor, Department of Electrical Engineering, VNIT Nagpur South Ambazari Road, Nagpur – 440010, M.S., India Phone: +91 9826585016 (Mob), +91 712 2801339 (Off) e-mail: pradyumn.c@eee.vnit.ac.in

Important Dates:	
Submission deadline for Application Form (e-mail & Hard Copy)	28 Feb, 2022
Intimation to Shortlisted Candidates (via e-mail)	02 Mar, 2022
Written Test	07 Mar, 2022
Interview	08 Mar 2022